

# 2SP0115T2A0(C)-SEMIX603GB12M7P Data Sheet

Compact, high-performance, plug-and-play dual-channel IGBT driver based on SCALE<sup>™</sup>-2 technology for individual and parallel-connected modules

### Abstract

The SCALE<sup>™</sup>-2 plug-and-play driver 2SP0115T2A0-SEMIX603GB12M7P / 2SP0115T2A0C-SEMIX603GB12M7P (Coated version using ELPEGUARD SL 1307 FLZ/2 from Lackwerke Peters with a typical thickness of 50µm) is a compact dual-channel intelligent gate driver designed for Semikron's SEMIX 3p IGBTs SEMIX603GB12M7P. The driver features an electrical interface with a built-in DC/DC power supply.

For drivers adapted to other types of high-power and high-voltage IGBT modules, refer to

www.power.com/products/scale-2-plug-and-play-drivers

Features	Applications
<ul> <li>Plug-and-play solution</li> <li>Allows parallel connection of IGBT modules</li> <li>Shortens application development time</li> <li>Extremely reliable; long service life</li> <li>Built-in DC/DC power supply</li> <li>20-pin flat cable interface</li> <li>Duty cycle 0 100%</li> <li>Active clamping of V<sub>ce</sub> at turn-off</li> <li>IGBT short-circuit protection</li> <li>Monitoring of supply voltage</li> <li>Safe isolation to EN 50178</li> <li>UL compliant</li> <li>Suitable for SEMIX603GB12M7P</li> </ul>	<ul> <li>Wind-power converters</li> <li>Industrial drives</li> <li>UPS</li> <li>Power-factor correctors</li> <li>Traction</li> <li>Railroad power supplies</li> <li>Welding</li> <li>SMPS</li> <li>Radiology and laser technology</li> <li>Research</li> <li>and many others</li> </ul>



### Safety Notice!

The data contained in this data sheet is intended exclusively for technically trained staff. Handling all high-voltage equipment involves risk to life. Strict compliance with the respective safety regulations is mandatory!

Any handling of electronic devices is subject to the general specifications for protecting electrostatic-sensitive devices according to international standard IEC 60747-1, Chapter IX or European standard EN 100015 (i.e. the workplace, tools, etc. must comply with these standards). Otherwise, this product may be damaged.

### **Important Product Documentation**

This data sheet contains only product-specific data. For a detailed description, must-read application notes and common data that apply to the whole series, please refer to "Description & Application Manual for 2SP0115T SCALE-2 IGBT Drivers" on <a href="https://www.power.com/products/scale-2-plug-and-play-drivers/2sp0115t">www.power.com/products/scale-2-plug-and-play-drivers/2sp0115T</a>

When applying SCALE-2 plug-and-play drivers, please note that these drivers are specifically adapted to a particular type of IGBT module. Therefore, the type designation of SCALE-2 plug-and-play drivers also includes the type designation of the corresponding IGBT module. These drivers are not valid for IGBT modules other than those specified. Incorrect use may result in failure.

### **Mechanical Dimensions**

Dimensions: Refer to "Description & Application Manual for 2SP0115T SCALE-2 IGBT Drivers" Mounting principle: Soldered onto SEMiX 3p SEMIX603GB12M7P IGBT module

### Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Unit
Supply voltage V <sub>CC</sub>	VCC to GND	0	16	V
Logic input and output voltages	To GND	-0.5	VCC+0.	5 V
SO <sub>x</sub> current	Fault condition, total current		20	mA
Gate peak current Iout	Note 1	-8	+15	Α
Average supply current Icc	Note 2		290	mA
Output power per gate	Ambient temperature $\leq$ 70°C (Note 3)		1.2	W
	Ambient temperature $\leq$ 85°C (Note 3)		1	W
Switching frequency f			9	kHz
Test voltage (50Hz/1min.)	Primary to secondary (Note 16)		3800	VAC(eff)
	Secondary to secondary (Note 16)		3800	VAC(eff)
DC-link voltage	Note 4		800	V
dV/dt	Rate of change of input to output voltage		50	kV/µs
Operating voltage	Primary/secondary, secondary/secondary		1200	Vpeak



Parameter	Remarks	Min	Max	Unit
Operating temperature	Note 20	-20	85	°C
Storage temperature	Note 21	-40	50	°C
Surface temperature	Only 2SP0115T2A0C- SEMIX603GB12M7P (Note 22)		125	°C

### **Recommended Operating Conditions**

Parameter	Remarks	Min	Тур	Max	Unit
Supply voltage V <sub>cc</sub>	To GND	14.5	15	15.5	V
Resistance from TB to GND SO <sub>x</sub> current	Blocking time $\neq$ 0, ext. value Fault condition, 3.3V logic	128		∞ 4	kΩ mA

# **Electrical Characteristics**

Power Supply	Remarks	Min	Тур	Max	Unit
Supply current Icc	Without load		33		mA
Efficiency η	Internal DC/DC converter		85		%
Coupling capacitance Cio	Primary side to secondary side, total, per channel		23		pF
Power Supply Monitoring	Remarks	Min	Тур	Max	Unit
Supply threshold Vcc	Primary side, clear fault	11.9	12.6	13.3	V
	Primary side, set fault (Note 5)	11.3	12.0	12.7	V
Monitoring hysteresis	Primary side, set/clear fault	0.35			V
Supply threshold V <sub>isox</sub> -V <sub>eex</sub>	Secondary side, clear fault	12.1	12.6	13.1	V
	Secondary side, set fault (Note 6)	11.5	12.0	12.5	V
Monitoring hysteresis	Secondary side, set/clear fault	0.35			V
Supply threshold Veex-VCOMx	Secondary side, clear fault	5	5.15	5.3	V
	Secondary side, set fault (Note 6)	4.7	4.85	5	V
Monitoring hysteresis	Secondary side, set/clear fault	0.15			V
Logic Inputs and Outputs	Remarks	Min	Тур	Max	Unit
Input impedance	V(INx) > 3V (Note 7)	3.5	4.1	4.6	kΩ
Turn-on threshold	V(INx) (Note 8)	-	2.6		V
Turn-off threshold	V(INx) (Note 8)		1.3		V
SOx output voltage	Fault condition, I(SOx) < 8mA			0.7	V



Vce-monitoring threshold Response time Delay to IGBT turn-off Blocking time <b>Timing Characteristics</b> Turn-on delay t <sub>d(on)</sub> Turn-off delay t <sub>d(off)</sub> Jitter of turn-on delay Jitter of turn-off delay Output rise time t <sub>r(out)</sub> Output fall time t <sub>f(out)</sub> Dead time between outputs Jitter of dead time	Between auxiliary terminals DC-link voltage > 550V (Note 9) After the response time (Note 10) After fault (Note 11) <b>Remarks</b> Note 12 Note 12 Note 12 Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)	Min	10.2 5.4 1.4 90 <b>Typ</b> 75 65 ±2 ±4 5	Max	V µs ms <b>Unit</b> ns ns ns
Delay to IGBT turn-off Blocking time <b>Timing Characteristics</b> Turn-on delay t <sub>d(on)</sub> Turn-off delay t <sub>d(off)</sub> Jitter of turn-on delay Jitter of turn-off delay Output rise time t <sub>r(out)</sub> Output fall time t <sub>f(out)</sub> Dead time between outputs	After the response time (Note 10) After fault (Note 11) <b>Remarks</b> Note 12 Note 12 Note 18 Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)	Min	1.4 90 <b>Typ</b> 75 65 ±2 ±4	Max	μs ms <b>Unit</b> ns ns
Blocking time <b>Timing Characteristics</b> Turn-on delay t <sub>d(on)</sub> Turn-off delay t <sub>d(off)</sub> Jitter of turn-on delay Jitter of turn-off delay Output rise time t <sub>r(out)</sub> Output fall time t <sub>f(out)</sub> Dead time between outputs	After fault (Note 11) Remarks Note 12 Note 12 Note 12 Note 18 Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)	Min	90 <b>Typ</b> 75 65 ±2 ±4	Max	ms Unit ns ns
Timing CharacteristicsTurn-on delay $t_{d(on)}$ Turn-off delay $t_{d(off)}$ Jitter of turn-on delayJitter of turn-off delayOutput rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	Remarks           Note 12           Note 12           Note 18           Gx to Ex (Note 13)           Gx to Ex (Note 13)	Min	<b>Typ</b> 75 65 ±2 ±4	Max	Unit ns ns
Turn-on delay $t_{d(on)}$ Turn-off delay $t_{d(off)}$ Jitter of turn-on delay Jitter of turn-off delay Output rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	Note 12 Note 12 Note 18 Note 18 $G_x$ to $E_x$ (Note 13) $G_x$ to $E_x$ (Note 13)	Min	75 65 ±2 ±4	Max	ns ns
Turn-off delay $t_{d(off)}$ Jitter of turn-on delay Jitter of turn-off delay Output rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	Note 12 Note 18 Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)		65 ±2 ±4		ns
Jitter of turn-on delay Jitter of turn-off delay Output rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	Note 18 Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)		±2 ±4		
Jitter of turn-off delay Output rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	Note 18 G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)		±4		ns
Output rise time $t_{r(out)}$ Output fall time $t_{f(out)}$ Dead time between outputs	G <sub>x</sub> to E <sub>x</sub> (Note 13) G <sub>x</sub> to E <sub>x</sub> (Note 13)		-		
Output fall time $t_{f(out)}$ Dead time between outputs	$G_x$ to $E_x$ (Note 13)		F		ns
Dead time between outputs			5		ns
-	Lafe building used (Nate 10)		10		ns
litter of dead time	Half-bridge mode (Note 19)		3		μs
	Half-bridge mode		±50		ns
Transmission delay of fault state	Note 14		400		ns
Outputs	Remarks	Min	Тур	Max	Unit
Turn-on gate resistor R <sub>g(on)</sub>	Note 15		1.075		Ω
Turn-off gate resistor Rg(off)	Note 15		1.55		Ω
Gate voltage at turn-on			15		V
Gate-voltage at turn-off	P=0W		-9.2		V
	P=1.2W		-7.1		V
Gate resistance to COMx			4.7		kΩ
Electrical Isolation	Remarks	Min	Тур	Max	Unit
Test voltage (50Hz/1s)	Primary to secondary side (Note 16)	3800	3850	3900	V <sub>eff</sub>
2 ,		3800	3850	3900	$V_{\text{eff}}$
Partial discharge extinction volt.					Vpeak
					Vpeak
Creepage distance					mm
					mm
					mm
Clearance distance	-				mm
					mm
					mm
Outputs Turn-on gate resistor R <sub>g(on)</sub> Turn-off gate resistor R <sub>g(off)</sub> Gate voltage at turn-on Gate-voltage at turn-off Gate resistance to COMx	Remarks Note 15 Note 15 P=0W P=1.2W	<b>Min</b> 3800	<b>Typ</b> 1.075 1.55 15 -9.2 -7.1 4.7 <b>Typ</b> 3850	<b>Max</b> 3900	

All data refer to  $+25^{\circ}$ C and V<sub>CC</sub> = 15V unless otherwise specified



#### Footnotes to the Key Data

- 1) The gate current is limited by the gate resistors located on the driver.
- 2) If the specified value is exceeded, this indicates a driver overload. It should be noted that the driver is not protected against overload.
- 3) If the specified value is exceeded, this indicates a driver overload. It should be noted that the driver is not protected against overload. From 70°C to 85°C, the maximum permissible output power can be linearly interpolated from the given data.
- 4) This limit is due to active clamping. Refer to the "Description & Application Manual for 2SP0115T SCALE-2 IGBT Drivers".
- 5) Undervoltage monitoring of the primary-side supply voltage (VCC to GND). If the voltage drops below this limit, a fault is transmitted to the corresponding outputs and the IGBTs are switched off.
- 6) Undervoltage monitoring of the secondary-side supply voltage (Visox to Veex and Veex to COMx which correspond with the approximate turn-on and turn-off gate-emitter voltages). If the corresponding voltage drops below this limit, the IGBT is switched off and a fault is transmitted to the corresponding output.
- 7) The input impedance can be modified to values  $<18 \text{ k}\Omega$  (customer-specific solution).
- 8) Turn-on and turn-off threshold values can be increased (customer-specific solution).
- 9) The resulting pulse width of the direct output of the gate drive unit for short-circuit type I (excluding the delay of the gate resistors) is the sum of response time plus delay to IGBT turn-off.
- 10) The turn-off event of the IGBT is delayed by the specified time after the response time.
- 11) Factory set value. The blocking time can be reduced with an external resistor. Refer to the "Description & Application Manual for 2SP0115T SCALE-2 IGBT Drivers".
- 12) Measured from the transition of the turn-on or turn-off command at the driver input to direct output of the gate drive unit (excluding the delay of the gate resistors).
- 13) Output rise and fall times are measured between 10% and 90% of the nominal output swing with an output load of  $10\Omega$  and 40nF. The values are given for the driver side of the gate resistors. The time constant of the output load in conjunction with the present gate resistors leads to an additional delay at the load side of the gate resistors.
- 14) Transmission delay of the fault state from the secondary side to the primary status outputs.
- 15) The gate resistors can be leaded or surface mounted. Power Integrations reserves the right to determine which type will be used. Typically, higher quantities will be produced with SMD resistors and small quantities with leaded resistors.
- 16) HiPot testing (= dielectric testing) must generally be restricted to suitable components. This gate driver is suited for HiPot testing. Nevertheless, it is strongly recommended to limit the testing time to 1s slots as stipulated by EN 50178. Excessive HiPot testing at voltages much higher than  $850V_{AC(eff)}$  may lead to insulation degradation. No degradation has been observed over 1min. testing at  $3800V_{AC(eff)}$ . The transformer of every production sample shipped to customers has undergone 100% testing at the given value or higher (<  $5100V_{AC(eff)}$ ) for 1s.
- 17) Partial discharge measurement is performed in accordance with IEC 60270 and isolation coordination specified in EN 50178. The partial discharge extinction voltage between primary and either secondary side is coordinated for safe isolation to EN 50178.
- 18) Jitter measurements are performed with input signals INx switching between 0V and 15V referred to GND, with a corresponding rise time and fall time of 8ns.
- 19) Note that the dead time may vary from sample to sample. A tolerance of approximately ±20% may be expected. If higher timing precisions are required, Power Integrations recommends using direct mode and generating the dead time externally.
- 20) A version with extended operating temperature range of -40°C...85°C (2SP0115T2B0) can also be supplied.
- 21) The storage temperature inside the original package (1) or in case the coating material of coated products may touch external parts (2) must be limited to the given value. Otherwise, it is limited to 90°C.
- 22) The component surface temperature, which may strongly vary depending on the operating condition, must be limited to the given value for coated driver versions to ensure long-term reliability of the coating material.



### **RoHS Statement**

We hereby confirm that the product supplied does not contain any of the restricted substances according Article 4 of the RoHS Directive 2011/65/EU in excess of the maximum concentration values tolerated by weight in any of their homogeneous materials.

Additionally, the product complies with RoHS Directive 2015/863/EU (known as RoHS 3) from 31 March 2015, which amends Annex II of Directive 2011/65/EU.

### Legal Disclaimer

The statements, technical information and recommendations contained herein are believed to be accurate as of the date hereof. All parameters, numbers, values and other technical data included in the technical information were calculated and determined to our best knowledge in accordance with the relevant technical norms (if any). They may base on assumptions or operational conditions that do not necessarily apply in general. We exclude any representation or warranty, express or implied, in relation to the accuracy or completeness of the statements, technical information and recommendations contained herein. No responsibility is accepted for the accuracy or sufficiency of any of the statements, technical information, recommendations or opinions communicated and any liability for any direct, indirect or consequential loss or damage suffered by any person arising therefrom is expressly disclaimed.



### **Ordering Information**

Our international terms and conditions of sale apply.

Power Integrations Driver Type #	Related IGBT
2SP0115T2A0-SEMIX603GB12M7P (Temperature range –20°C85°C) 2SP0115T2A0C-SEMIX603GB12M7P (Temperature range –20°C85°C, conformal coating)	SEMIX603GB12M7P SEMIX603GB12M7P

Product home page: www.power.com/products/scale-2-plug-and-play-drivers/2sp0115t

### **Information about Other Products**

#### For other drivers, evaluation systems product documentation and application support

Please click: <u>www.power.com/products/gate-drivers</u>

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